

V H Type

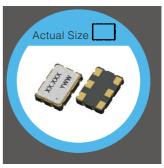
7.0 x 5.0 mm SMD Voltage Controlled **Crystal Oscillator**

FEATURE

- Typical 7.0 x 5.0 x 1.45 mm 6 pads ceramic SMD package.
- Tight symmetry (45 to 55%) available.
- Output frequency up to 250 MHz.
- Tri-state enable/disable

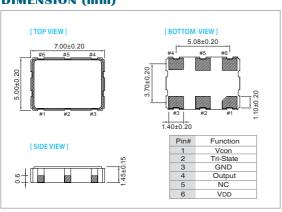
TYPICAL APPLICATION

- Set-top Box, HDTV
- WiMAX/WLAN
- xDSL/ VoIP, Cable modem

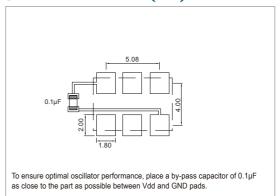


RoHS Compliant

DIMENSION (mm)



SOLDER PAD LAYOUT (mm)



FIFCTRICAL SPECIFICATION

Parameter	2.5V / 3.3 V		Limit
Farameter	Min.	Max.	Unit
Supply Voltage Variation (VDD)	VDD-5%	VDD+5%	V
Frequency Range	10	250	MHz
Absolute Pulling Range (APR)	±50	_	ppm
Control Voltage Range	0.3	3.0	V
Supply Current			
10 MHz ≦ Fo < 160 MHz	-	40	mA
160 MHz ≦ Fo ≦ 250 MHz	_	50	
Output Level (CMOS)			
Output High (Logic "1")	2.97	_	v
Output Low (Logic "0")	-	0.33	
Transition Time: Rise/Fall Time+			
10 MHz ≤ Fo ≤ 250 MHz	_	2	nSec
Start Time	_	2	mSec
Tri-State (Input to Pin 2)			
Enable (High voltage or floating)	2.31	_	v
Disable (Low voltage or GND)	_	0.99	-
Period Jitter (Pk-Pk)	_	150	pSec
RMS Phase Jitter (12kHz~20MHz) (fractional mode)	0.8	1.5	pSec
RMS Phase Jitter (12kHz~20MHz) (integer mode)	0.6	1.2	pSec
Linearity	_	10	%
Modulation Bandwidth (BW)	10	_	kHz
Input Impedance	1000	_	kΩ
Phase Noise@155.52MHz 100 Hz	-75		
1 kHz	-105 dBc/		dBc/Hz
10 kHz	-125		
Aging (@ 25°C 1st year)	_	±3	ppm

Standard frequencies are frequencies which the crystal has been designed and does not imply a stock position.

FREQ. STABILITY vs. TEMP. RANGE

Temp. (°C)	±25	±50
-10 ~ +60	0	0
-20 ~ +70	0	0
-40 ~ +85	\triangle	0
-40 ~ +105	X	Δ

^{* ○:} Available △: Conditional X: Not available

Note: not all combination of options are available. Other specifications may be available upon request.

⁺ Transition times are measured between 10% and 90% of VDD, with an output load of 15pF.

^{*} Inclusive of calibration @ 25 °C, operating temperature range, input voltage variation, load variation, aging (1st year), shock, and vibration